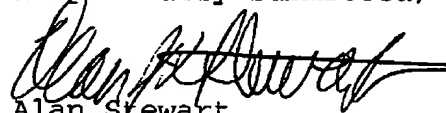## REMARKS

Claims 1, 2, 4-6, 8 and 9 are pending in the application. Claims 1, 2, 4-6, 8 and 9 are rejected.

Claim 1 includes "... a multiplexer coupled between the memory and the plurality of interconnection pads wherein the universal interface device concurrently interfaces with the plurality of different peripheral devices using time multiplexing of the plurality of interconnection pads." The references of record do not show, teach, or suggest the above recited limitation of claim 1. The Nilsson reference does not disclose a multiplexer for time multiplexing coupled between the memory and the plurality of interconnection pads wherein the universal interface device concurrently interfaces with the plurality of different peripheral devices. The Nilsson reference does not have a time multiplexer coupled between the memory and the interconnection pads. Claims 2, 4-6, 8, and 9 depend from claim 1. Therefore claims 1, 2, 4-6, 8 and 9 are believed to be allowable over the references of record.

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It is believed that the above remarks and amendments are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,

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